U.S. Patent Application Serial No. 10/084,369 Response filed April 25, 2005 Reply to OA dated January 24, 2005

## **AMENDMENTS TO THE SPECIFICATION:**

Amend the specification as follows:

Replace the paragraph beginning on page 11 line 11 with the following rewritten paragraph:

If (a+b+c+d), (a-b+c-d), (a+b-c-d), (a-b-c+d) are negative then Xi has two subtracted therefrom and it is then divided by four and then has its dismal decimal part truncated and it is thus rounded off (step S108).

Replace the paragraph beginning on page 13 line 2 with the following rewritten paragraph:

For each type of Hadamard block thus classified, a process is provided, as will be described hereinafter:

For an Hadamard block of type 1:

if (a+b+c+d), (a-b+c-d), (a+b-c-d), (a-b-c+d) are 0 or more (step S206) then Xi has two added thereto and is then divided by four (step S210) and it has its dismal decimal part truncated and it is thus rounded off (step S218); and

if (a+b+c+d), (a-b+c-d), (a+b-c-d), (a-b-c+d) are negative (step S206) then Xi has two subtracted therefrom and is then divided by four (step S212) and it has its dismal decimal part truncated and it is thus rounded off (step S218).

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> Replace the paragraph beginning on page 13 line 15 with the following rewritten paragraph: For an Hadamard block of type 2:

if (a+b+c+d), (a-b+c-d), (a+b-c-d), (a-b-c+d) are 0 or more (step S208) then Xi has one added thereto and is then divided by four (step S214) and it has its dismal decimal part truncated and it is thus rounded off (step S218); and

if (a+b+c+d), (a-b+c-d), (a+b-c-d), (a-b-c+d) are negative (step S208) then Xi has one subtracted therefrom and is then divided four, and has its dismal decimal part truncated and thus rounded off (step S218).

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